

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-7 (canceled)

Claim 8 (currently amended): An apparatus for controlling traffic over a network, comprising:

a switching processor, including a plurality of ports connectable to a network line and packet counter registers for storing counting information on packets ingressed and egressed through each of the ports of said plurality of ports and for controlling ingress and egress packet traffic volume for each of ~~said plurality of the~~ ports in response to an input traffic control command; and

a controller for registering traffic volume for each of ~~said plurality of the~~ ports in an internal register, said traffic volume being entered by a user through a data input unit as a user value, and for comparing the user value for each of said plurality of ports with a value in a respective one of said packet counter registers for said each port so as to output said input traffic control command for said each port to said switching processor.

Claim 9 (previously presented): The apparatus according to claim 8, wherein said input traffic control command is a control command that enables said packets ingressed or egressed through said each port to be queued, dropped, or paused.

Claim 10 (currently amended): An apparatus for controlling traffic over a network, comprising:

a switching processor, including a plurality of ports connectable to a network line and ~~a~~-packet counter ~~register~~ registers for storing counting information on packets ingressed and/or egressed through each of the ports of said plurality of ports and for controlling ingress and/or egress packet traffic volume for each of the ~~said plurality of~~ ports in response to an input traffic control command; and

a controller for registering traffic volume for ~~said plurality of each of the~~ ports in an internal register, said traffic volume being entered by a user through a data input unit as a user value, and for comparing the user value for ~~said plurality of each of the~~ ports with a value in a respective one of said packet counter registers for ~~said plurality of each of the~~ ports so as to output said input traffic control

command for said plurality of ports to said switching processor.

Claim 11 (previously presented): The apparatus according to claim 10, wherein said ingress and/or egress traffic volume is controlled via a token bucket, which is shared between the ports of said plurality of ports.

Claim 12 (previously presented): The apparatus according to claim 10, wherein said input traffic control command is a control command that enables said packets ingressed or egressed through said each port to be queued, dropped, or paused.

Claim 13 (previously presented): The apparatus according to claim 11, wherein said input traffic control command is a control command that enables said packets ingressed or egressed through said each port to be queued, dropped, or paused.

Claim 14 (currently amended): A method for controlling a traffic volume ingressed or egressed via a port or a plurality of ports of a switching processor[[,]]:

entering a user value, by a user through a data input unit,
for a maximum traffic volume;

comparing said user value with a respective value for said
traffic volume, said respective value being written in a
packet counter register; and

issuing a traffic control command to said switching processor.

Claim 15 (previously presented): The method according to
claim 14, wherein said traffic volume relates to a plurality
of ports and said traffic volume is controlled via a token
bucket, which is shared between the ports of said plurality of
ports.

Claim 16 (previously presented): The method according to
claim 14, wherein a packet is dropped by setting the frame
size parameter smaller than the minimum Ethernet frame size.

Claim 17 (previously presented): The method according to
claim 15, wherein a packet is dropped by setting the frame
size parameter smaller than the minimum Ethernet frame size.